

Docket No.: 50023-136

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Hiroshi MATSUUCHI, et al.

Serial No.:

Group Art Unit:

Filed: April 04, 2001

Examiner:

For: DIGITAL DEVICE, DATA INPUT-OUTPUT CONTROL METHOD, AND DATA  
INPUT-OUTPUT CONTROL SYSTEM

*Patent*  
*9-19-01*  
*H. Little*

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, DC 20231

Sir:

Prior to examination of the above-referenced application, please amend the application as follows:

**IN THE CLAIMS:**

Please amend claim 4 and claim 5 as follows:

4. (Amended) A digital device according to [either one of] claim 1[ to 3], wherein the data is synchronous stream one.

5. (Amended) A digital device according to [either one of] claim 1[ to 3], wherein the bus interface is compliant with the standard of IEEE1394.

Please add new claims 13 through 16 as follows:

--13. A digital device according to claim 2, wherein the data is synchronous stream one.

*A2*